## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Yamamoto, et al.

Serial No.: 09/600,931

Filed: July 21, 2000

For: DAMASCENE

INTERCONNECTION AND SEMICONDUCTOR DEVICE

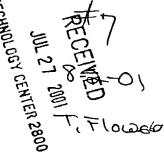
Commissioner for Patents Washington, DC 20231

Examiner: Eugene Lee

Group Art Unit: 2815

Docket: 362-43 PCT/US

Dated: July 18, 2001



I hereby certify this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to: Assistant Commissioner for Patents, Washington, D.G.

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Dated: 7/18/01 of Julie & Watt

## REQUEST FOR APPROVAL OF DRAWING CHANGES

Sir:

It is respectfully requested that the Examiner approve the changes to Figures 1-5, as shown in a marked-up copy of the same provided herewith. A duplicate copy of this sheet, along with a duplicate copy of marked-up Figures 1-5, are submitted herewith.

Respectfully submitted,

Rod S. Turner

Registration No.: 38,639 Attorney(s) for Applicant(s)

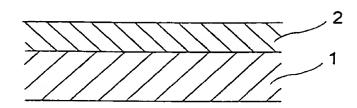
HOFFMANN & BARON, LLP 6900 Jericho Turnpike Syosset, New York 11753 (516) 822-3550 RST:jlw

137483\_1.DOC

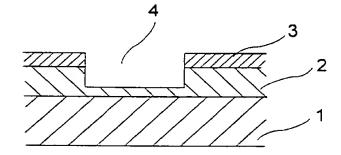
## FIG. 1 PRIOR ART

OK EL

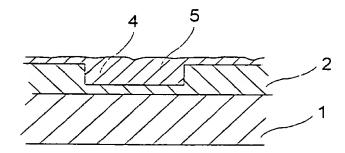
(a)



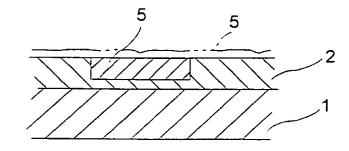
(b)



(c)



(d)



OK EL

FIG. 2 PRIOR ART

